FOS Study Material (Updated Jan 2021)

Telecommunication, Signal and Image Processing (Dr. Sesay)


Power and Control Systems (Dr. Zareipour – Power Systems and Dr. Westwick – Control Systems)

Power Electronics. Chapter 1, Chapter 7 (section 7-1 to 7-4), and Chapter 8 (section 8-1 to 8-4) from Power Electronics: Converters, Applications, and Design (2nd Ed.), N. Mohan, T.M. Undeland, and W.P. Robbins, Wiley.


Control Systems. Chapters 2, 3, 4, 5, 6, 8 and 13 from Modern Control Systems (12th Ed.), R.C. Dorf, and R.H. Bishop, Pearson.

Software Engineering (Dr. Moussavi)


- Database Architectures and Web
- Relational Model
- Relational Algebra
- SQL data manipulation
- SQL data definition
- Database Analysis and Design
- Entity Relationship Modelling
- Normalization
Data structures and algorithms. Chapters 3-5, 9, 10 from Data Structures and Algorithm Analysis in Java, M.A. Weiss. Pearson.
- Lists, stacks and queues
- Trees
- Hashing
- Graphs
- Algorithm design techniques

Object-oriented design and design patterns. Chapters: 2, 4-9 from Object oriented software engineering, practical development using UML and Java, T. Lethbridge, and R. Laganiere, McGraw-Hill.
Topics:
- Principles of object orientation
- Developing requirements
- Modelling with classes
- Using design patterns
- Focusing on users and their tasks
- Modelling interactions and behaviour
- Architecting and designing software

**Biomedical Engineering (Dr. Murari)**


**Circuits and Electronics (Dr. Murari)**


**Computer Engineering (Dr. Mike Smith) – Exam Not available for Winter 2020**

Review of computer architecture, microcontrollers and their instruction sets; Interfacing using common input/output devices – e.g. SPI, GPIO; Strategies for interrupt handling and exception handling; Interfacing combining code using functions implemented in high level and assembly languages; Real time operating systems; Software and hardware optimizations to achieve real time operations; Processor characteristics needed to match the requirements for typical DSP applications; Hardware and software optimization techniques including multiple busses; Superscalar and other highly parallel instruction sets, critical timing paths; Optimizing compilers and multi-processor operation. Digital Design and Computer Architecture (2nd Ed.), by D. Harris, and S.L. Harris, Morgan Kaufmann Publishers.

Interrupt handling, DMA and interfacing. Source material TBD.

**RF Circuits and Applied Electromagnetics (Dr. Vyas)**


Microwave amplifiers. Chapters 2-4 from Microwave Transistor Amplifiers: Analysis and Design (2nd Ed.), G. Gonzalez, Prentice Hall.

RF and Microwave Circuits. Chapters 1-7, 10, 12 from Microwave Engineering (4th Ed), David M.Pozar, Wiley.